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	Filing Date	November 28, 2000
	First Named Inventor	Raymond C. Pang
	Examiner Name	Minh Dieu T. Nguyen
	Art Unit	2137
	Patent No.	
Attorney Docket Number		X-805-6 US

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X-805-6 US
09/724,975

PATENT
Conf. No. 7824

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Raymond C. Pang et al.
Assignee: Xilinx, Inc.
Title: Programmable Logic Device with Method of Preventing Readback
Serial No.: 09/724,975 Filing Date: 11/28/2000
Examiner: Minh Dieu T. Nguyen Art Unit: 2137
Docket No.: X-805-6 US Conf. No.: 7824

Mail Stop Amendment
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AMENDMENT IN RESPONSE TO THE SECOND OFFICE ACTION

In response to the Second Office Action mailed from the Patent Office on February 16, 2005, Applicants submit the following amendments and remarks.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 7 of this paper.